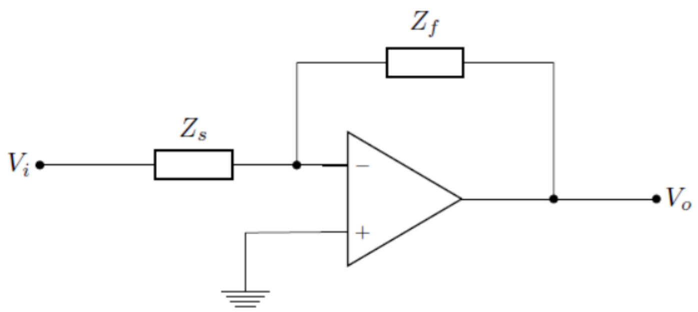
EE233 HW9

Dec. 2nd

Due Date: Dec. 9th

1. Consider the circuit below.
   (a) Design a HPF with a passband gain of 8dB and cutoff frequency of 4kHz using 250nF capacitor. Draw the circuit diagram and label all components.
   (b) If the value of the feedback resistor in the filter is changed but the value of the resistor in the forward path is unchanged, what characteristic of the filter is changed? Briefly justify your answer.
   (c) Sketch the Bode magnitude and phase plots for the filter.



2. Consider the circuit in problem 1.
   (a) Design a HPF with a passband gain of 14dB and a cutoff frequency of 8kHz using a 3.9nF capacitor. Draw the circuit diagram and label all components.
   (b) Using only three components from Appendix H of the textbook (file posted on Canvas), design a HPF with a cutoff frequency and passband gain as close as possible to the specifications above in part (a). Draw the circuit diagram and label all component values.
   (c) Calculate the percent error in this new filter's cutoff frequency and passband gain when compared to the values specified in part (a).
   (d) Sketch the Bode magnitude and phase plots for both filter designs on the same set of axes.

3. Using 10nF capacitors, design an active broadband first-order band-reject filter that has a lower cutoff frequency of 400Hz, an upper cutoff frequency of 4000Hz, and a passband gain of 0dB. Use prototype versions of the LPF and HPF in the design process (use scaling).

(a) Draw the circuit diagram of the filter and label all the components.

(b) Write the transfer function of the scaled filter.

(c) Use the transfer function to find $H(j\omega_0)$, where ω_0 is the center frequency.

(d) What is the passband gain (in dB) of the filter at the center frequency?

(e) Sketch the Bode magnitude and phase plots for the filter.

4. Design an active LPF with a passband gain of 4dB, a cutoff frequency of 1kHz, and a gain roll-off rate of -60dB/decade.

   (a) Calculate the resistor and capacitor values used in the circuit.

   (b) Draw the circuit diagram of the filter and label all the components.

   (c) Sketch the Bode magnitude and phase plots for the filter.

5. Your company has developed a new type of loud speaker system that uses very low power to reproduce sound. Like a home stereo system it users 3 independent speakers, the woofer, midrange, and tweeter, to reproduce the low, mid and high frequencies, respectively. The woofer is designed to reproduce frequencies up to 200Hz. The midrange delivers frequencies between 200Hz and 1400Hz. The tweeter is designed for frequencies above 1400Hz.

   You have been given the job of designing a filter system to separate the signal and deliver only the desired frequencies to each speaker. Others on your ream will take care of the rest (buffering of your input signal, level-adjustment to boost the filter output to the appropriate level, line conditioning to prevent RF interference, etc.) Also assume, for this exercise, that phase-distortion is not a problem. Each should filter should be designed separately.

   For the low frequency speaker uses an active low-pass filter with two cascaded first order filters to get sharper roll-off (a steeper cutoff in the frequency plot). The cutoff frequency should be 100Hz and should have unity gain in the passband. Make sure to take into account the shift in cutoff frequency that occurs when you cascade the two sections.

   For the midrange circuit implement a band-pass filter by cascading low-pass, high-pass, and inverting op-amp circuits. The resulting filter should pass frequencies from 100Hz to 1400Hz, and have a maximum gain of 10 (20dB).

   The tweeters your company developed require more precise frequency selection. Implement a 4^{th} order high pass Butterworth filter with a cutoff frequency of 1400Hz.

For each of the three filters you should provide:

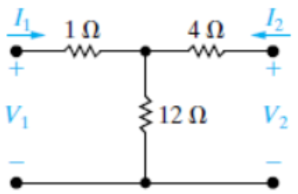
- A schematic of the op-amp circuit
- Specifications for all components used
- Transfer function H(s) for your circuit
- Magnitude only Bode Plot of the showing the frequency response of your circuit

Use reasonable values for each component. For ease of grading, use 1 μF capacitors when possible. Assume that you will use ideal op amps.

Bonus: Simulate your circuits in Multisim and show the frequency response.

6. Textbook Problem 18.2: Find the ‘z’ parameters for the circuit shown in Fig. P.18.2.

Figure P18.2



7. Textbook Problem 18.38: The z and y parameters for the resistive two-ports in Fig. P18.38 are given by:

$$z_{11} = \frac{35}{3}\ \Omega; \qquad y_{11} = 200\ \mu S;$$
$$z_{12} = -\frac{100}{3}\ \Omega; \qquad y_{12} = 40\ \mu S;$$
$$z_{21} = \frac{4}{3}\ k\Omega; \qquad y_{21} = -800\ \mu S$$
$$z_{22} = \frac{10}{3}\ k\Omega; \qquad y_{22} = 40\ \mu S;$$

Calculate v_o if v_g is 30mVDC.

Figure P18.38