

EE331 Devices and Circuits 1

Spring 2014

Homework 5

Due in class on Friday, May 9 2014.

1. Jaeger & Blalock Problem 4.27.
2. Jaeger & Blalock Problem 4.40.
3. Jaeger & Blalock Problem 4.44.
4. Jaeger & Blalock Problem 4.57.
5. Jaeger & Blalock Problem 4.71 (a).
6. Jaeger & Blalock Problem 4.93 (a).
7. Jaeger & Blalock Problem 4.111 (b).
8. Jaeger & Blalock Problem 4.127. Use the NMOS transistor parameters listed in Table 4.6 on p. 204.
9. Jaeger & Blalock Problem 4.134.
10. Jaeger & Blalock Problem 6.11.