

EE331 Devices and Circuits 1

Spring 2014

Homework 7

Assigned on Monday, May 19 2014.

Due in class on Friday, May 23 2014.

1. Jaeger & Blalock Problem 6.20.
2. Jaeger & Blalock Problem 6.132.
3. Jaeger & Blalock Problem 6.135.
3. Jaeger & Blalock Problem 6.148. In addition to solving using method in text, repeat using method introduced in lecture ($\Delta t = C\Delta V/I_{avg}$). If one of the transistors changes mode during the transition, divide up the voltage swing and then add the times for sub-intervals. Compare your results to MultiSim simulations.
5. Jaeger & Blalock Problem 6.150 (a,c,e,g).
6. Jaeger & Blalock Problem 6.152.
7. Jaeger & Blalock Problem 7.11.